REAL POWER FLOW CONTROL OF 2 BUS MC-UPFC USING FUZZY LOGIC CONTROLLERS

¹C. Boopalan, ²V. Saravanan, ³T. A. Raghavendiran, ⁴S. Deepaprasath

¹Research Scholar, Anna university, Chennai India.

² Professor/EEE, Arunai Engineering College, Tiruvannamalai, India

³Dean,Sri Venakateswara Engineering college for women, Tirupathi, India

⁴Assistant Porfessor/EEE,C. Abdul Hakeem College of Engineering and Technology, Vellore, India

Correspondence Author: boopalanchandran@gmai.com

Abstract

This paper deals with the design and testing of 2 Bus power system incorporated with Matrix Converter based Unified Power Flow Controller [MC-UPFC] to improve the transmission parameters such as real power and reactive power in both the generator side and the load side. The MC-UPFC is the rich topology in the Flexible AC Transmission System [FACTS] which is capable of controlling both the transmission parameters voltage injected and the angle of injection simultaneously with the switching technique of Direct power control by the smooth sliding control which gives less ripple in the injecting control parameters such as control voltage $[V_C]$ and voltage angle $[\alpha]$. Hence the sliding surface and relevant control switching state of the MC can be controlled by the Fuzzy Logic Controller [FLC] which gives the robust and autonomous decision making in the selection of the appropriate switching state for the effective real power control in the power system. The work has been carried out in the MATLAB Simulink simulator which gives the finest controlling features and simple design procedures and easy monitoring of the output

Index terms: Matrix converter (MC), Unified power flow controller (UPFC), Fuzzy logic Controller (FLC).

INTRODUCTION:

In the modern Engineering world the demand of electricity is grown up and the quality concern of the electricity is also developed in the consumers mind because of the wide increase in electronic equipment usage in the day to day life. The power system which comprises of generation transmission and distribution has to be optimized for the effective utilization of the electric energy. The transmission system is the major observable area for the scope of research because the loss and quality is the major crisis in the system. Facts devices are the major solution providers in ensuring the distribution parameters [1-2].

There are various facts devices are available in the power system, among them UPFC is the device which is having more advantages over all other FACTS devices. UPFC comprises of two converters one connected in shunt with transmission and other converter is connected in series with the transmission line and both converters connected with the Direct Current link. UPFC is capable for real time control and dynamic compensation in AC transmission system [3-10].

The application of power electronics in power system makes the static and smooth control possible in power system. In the power electronics controllers the matrix converter has attracted more and more attentions due to the advantageous features such as sinusoidal input and output current control. Bidirectional energy flow controllable, lots of modulation techniques are available under which space vector based pulse width modulation technique with direct power control using sliding mode control is performing better. The matrix converter is placed in series with the transmission line. This results in the whole power being transmitted through MC thereby demanding higher device rating [11-17].

The matrix converter concept was first published in mid of 1970's. The circuit was considered to be a cycloconverter where the devices were fully controllable. which also called as forced commutation cycloconverter. Matrix converters are direct ac to ac converters that are used to convert available ac supply into desirable ac output with variable voltage and variable frequency. This converter are used for sinusoidal input/output current and voltages, input power factor control, regeneration capability, compact circuitry and less weight, Less noise. Works on direct duty ratio control. MC is more robust and reliable it can act as full converter. The direct Pulse width modulation control is implemented in this paper [18-24]. The MC-UPFC topology comes without any bulky reactive element except for lighter components such as bipolar switches. Thus reduces the cost. Construction is simple less components compared with conventional UPFC [25-26].

The fuzzy logic controller (FLC) is an adaptive control algorithm which is an emerging trend in the control and decision making system. In our proposed system the switching scheme based on the need of the controller will be provided by the FLC system as FLC based MC-UPFC. The complex space vector model is presented along with space vector PWM with fuzzy logic algorithm. The proposed FLC based MC-UPFC is a 3 limb 9 switches connected to the transmission system through a shunt and series transformer. The switching scheme of the MC is controlled by a FLC. The proposed system will precisely control the power flow by controlling the injecting bus voltage the injecting angle. Fuzzy system is designed using Fuzzy Inference System [21-29].

In the following sections the functioning and performance of FLC based MC-UPFC will be discussed.

SYSTEM MODELING: UPFC WITH TRANSMISSION SYSTEM:

The proposed power system was represented in the figure: 1 consists of sending end and receiving end generators G_1 and G_2 respectively with the voltages V_{G1} and V_{G2} . The power system has a transmission line and it can be realized as an equivalent resistor and inductor in series namely R and L. The load is connected in the power system and represented with Z_L . The MC-UPFC was connected to the transmission line through a shunt transformer (T_{SH}) and series transformer (T_{SE}) to control the power system. The Fuzzy block is used to provide the switching control signals. The Power system parameters are measured in the buses namely B_1 and B_2 .

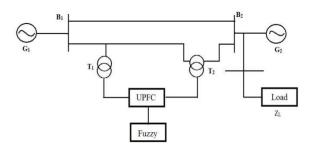


Figure:1 Power system with FLC based MC-UPFC

WORKING OF MC- UPFC SYSTEM:

Figure: 2 shows the reduced model of 3-phase MC-UPFC with a generator block named as generator with the voltage of V_g , MC-UPFC shown with its controlled injecting voltage V_c , transmission line represented with TL block and the line voltage as V_t , the influence of the

controlled injected voltage through the transformer is represented as V_S and it was fed to the impedance load shown as Z_L .

The voltage and current at various points and its direction can be understood with the help of the figure: 2. For system modeling, the Generating sources, series transformers and parallel transformers are all considered ideal. The matrix converter is constructed with the help of bi-directional ideal switches and represented as a controllable voltage source, The matrix converter is connected to transmission system with the input through parallel connected transformer and output through the series connected transformer. In the MC-UPFC all 9 bidirectional switches are arranged to ensure the possibility of each phase voltage can be feed to any phase by the controlled injected volt to the load independently. The real power of the system can be controlled injecting of voltage to the transmission system from the UPFC with two control parameter namely amplitude V_C and phase angle α.

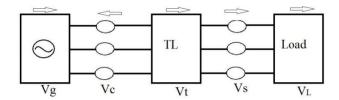


Figure:2 Equivalent circuit of MC-UPFC in Power system.

In our Proposed system the matrix converter will replace the three devices called shunt converter, dc-link and series converter. The MC-UPFC model has a major block as 3x3 a three limb 9 switch converter which can be controlled by a Direct Power control method to maintain the line real power. The switched voltage will be observed by the shunt transformer (T_{SH}) and injected through the series transformer (T_{SE}). The 9 bi-directional switches can turn ON and turn OFF according to the control signal. These switches allows any phase voltage to be injected to any phase in the transmission line.

Applying the kirchoff's law to the UPFC transmission network, the power delivered can be estimated with the dq coordinates procedure.

$$I_d = \int \left((\omega Iq) - \left(rac{R_2}{L_2}
ight) I_d^2 + I \left(rac{1}{L_2}
ight) (V_{Ld} - V_{Rod})
ight)$$
 (1)

$$I_q = -\int \left((\omega I_d) + \left(rac{R_2}{L_2}
ight) I_q - rac{1}{L_2} (V_{Lq} - V_{R0q})
ight)$$

The simplified real power and reactive power can be equated with the power system parameter [19] as given below.

Real power
$$P = V_d \cdot I_d$$
 (3)
Reactive Power $Q = -V_d \cdot I_q$ (4)

The Error e_p is obtained from the difference between the pre-set reference power in per unit and the actual power available in the load bus B2 the same procedure is applied to identify the e_q reactive power.

The stability conditions mentioned in equation (5) & (6), has to be satisfied to control the real and reactive power [19-21]. The sliding mode function S(ep,t) is quantized by the hysteresis comparator three level as (-1),(0) and (+1), similarly S(eq,t) sliding mode reactive power error also obtained.

$$S_p(e_p, t) \cdot dS_p(e_p, t) < 0$$
 (5)
 $S_q(e_q, t) \cdot dS_q(e_q, t) < 0$ (6)

To control the real power in the load bus the following algorithm is implemented.

1. If
$$S_p(e_p, t) < 0 \Rightarrow \dot{S}_p(e_p, t) < 0 \Rightarrow P_{out} < P_{ref}$$
 to increase P.

2. If
$$S_q\left(e_q,\,t\right)<0 \Rightarrow \dot{S}_q\left(e_q,\,t\right)<0 \Rightarrow P_{out}>P_{ref}$$
 3. then chose vector suitable to increase P.

4.If $S_p(e_p, t) = 0 \Rightarrow P_{out} = P_{ref}$ then chose vector which un-change P. Where, Pout is actual power, Pref is the reference power assigned to the controller.

To ensure smooth voltage injection sliding mode technique is implemented. To achieve the sliding mode voltage control, we need to select the right switch so that the voltage injection will give less transient peak over shoot in the load side. To understand the technique we need split the single cycle of the 3-phase voltage into 12 segments in time frame. To control with the space vector modulation we must need to know the actual instantaneous position of the waveform as to be identified [12-20].

$$Control\ switch\ arrangement\ C = \begin{bmatrix} c1 & c2 & c3 \\ c4 & c5 & c6 \\ c7 & c8 & c9 \end{bmatrix}$$

To ease the understanding of the switching state table, let we assume the matrix of switches are arranged as above (3x3) matrix 'C' with the switch numbers c1 to c9. The control the injecting voltage and angle 21 switching schemes are used and it was presented in the Table:1.

Switching	Output v	Switches in			
state	V_{A}	$V_{\rm B}$	$V_{\rm C}$	On state	
1	A	В	В	1,5,6	
2	В	A	A	4,2,3	
3	В	C	C	4,8,9	
4	С	В	В	7,5,6	
5	С	A	A	7,2,3	
6	A	С	С	1,8,9	
7	В	A	В	4,2,6	
8	A	В	A	1,5,3	
9	С	В	С	7,5,9	
10	В	С	В	4,8,6	
11	A	С	A	1,8,3	
12	С	A	С	7,2,9	
13	В	В	A	4,5,3	
14	A	A	В	1,2,6	
15	С	С	В	7,8,6	
16	В	В	С	4,5,9	
17	A	A	С	7,8,3	
18	С	С	A	1,5,9	
19	A	A	A	1,2,3	
20	В	В	В	4,5,6	
21	С	С	С	7,8,9	

Table: 1-sliding mode DPC switching state

For the execution of the above table the FLC will give the decision in terms of the switching sequence number and with reference to this the switching pattern will be switched in the matrix converter.

In case, if the switching state is "6" we need to switch on 1,8,9 switches in Matrix converter. input reactive power have to be considered for selection of switching states. The sign of the matrix reactive power Qi can be determined by knowing the location of the output currents.

The possibilities of switching sequence based on the Voltage location, Current location and the sign of input reactive power is stated in the table:2.

		I ₀ 12 & I ₀ 1		I ₀ 2 & I ₀ 3		I ₀ 4 & I ₀ 5		I ₀ 6 & I ₀ 7		I ₀ 8 & I ₀ 9		I ₀ 10 & I ₀ 11	
		Cqi(v1)		Cqi(v2)		Cqi(v3)		Cqi(v4)		Cqi(v5)		Cqi(v6)	
Ca	Cb	P	M	P	M	P	M	P	M	P	M	P	M
L	H	19	24	21	24	20	21	20	23	23	22	22	19
L	Z	8	11	8	11	10	7	7	12	12	9	8	9
L	L	13	18	18	15	15	14	14	17	16	17	13	16
Z	H	19	24	19	24	20	21	20	23	23	22	8	19
Z	Z	9	10	15	19	17	18	10	9	19	20	17	18
Z	L	23	20	23	22	22	19	24	19	21	24	20	21
H	H	17	4	14	17	13	16	18	13	18	15	15	14
Н	Z	12	7	12	9	8	9	11	8	10	11	10	7
Н	L	23	20	23	22	22	19	24	19	21	24	20	24

Table: 2-sliding mode Sliding mode DPC switching state

Table: 2 illustrate the sample of switching scheme possibilities in each column. If sliding error of real and reactive power are quantized into three level called as high (H), low (L) and zero (Z). There will be 9 different combinational possibilities as mentioned in the column 1 and column 1 in the table: 2 as the voltage location was segmented into 12 regions each having 30 degree span from V1 to V12, similarly the output current location also segmented and named from I1 to I12. Based on the combinational possibilities switching patterns from 1 to 18 stated in the table: 1 is selected and if the error is zero then the null vector switching pattern can selected which is highlighted from switching patterns from 19 to 21 in table: 1. To execute we need to switch on the switch number mention in the table-2. For example if the switching state is 7 we need to switch on (1,5,6) switches in Matrix converter.

MC-UPFC WITH FUZZY LOGIC CONTROL:

The power system proposed consists of a generator and the generated voltage is fed to the transmission line through the bus. The matrix converter with the 9 switches was connected through the shunt and series transformer. The UPFC can be controlled through the three phase relay which can be operated by the external control device so that the total UPFC system can be switched whenever it is required. At the end of the transmission line loads is which can receives the injected power parameters at the bus 2. The matrix converter is connected with the help of 9 bidirectional switches in three limbs operated by the switching schemes. The UPFC can be switched off/on with the timer based relay. A second order filter is connected in series with the UPFC to smoothen the injected power. A six winding three phase transformer with the transformer ratio K=1 is connected to the UPFC in parallel and the secondary winding of the transformer coil is connected in series with each phase so that the UPFC control voltage is

seriously injected to the transmission line to control the real power of the load. The external pulse is given to operate the UPFC with the time reference.

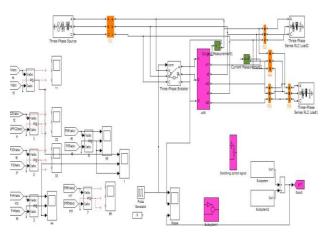


Figure: 3 Schematic diagram of a MC-UPFC

Control signal to the switches in the matrix converter is given through the selector switch in which the switching sequence have been feed with 21 different switching patterns. By feeding the control pattern number to the selector switch so that the power electronic switches operate accordingly.

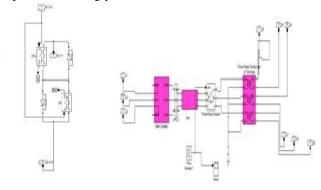


Figure: 4.(a) Bi-directional switch; (b) matrix converter with filter and series transformer.

Matrix converter is constructed with a bidirectional switch which is formed by two Insulated Gate Bi-polar Transistors (IGBT) switch connected back to back with a free-wheeling diode in anti-parallel connection as shown in the figure: (4.a) To form a matrix converter this kind of switch is connected in 3x3 matrix arrangement. Figure: (4.b) shows the matrix converter block and a filter arrangement to smoothen the ripple.

The selector switching arrangement gives the switching state as shown in the equation (8).

$$selector\ array = [c1\ c1\ c3\ c4\ c5\ c6\ c7\ c8\ c9]$$

Case 1: to switch on the pattern "6" in the table 2 the output phase voltages will be available as given in the equations (9), (10) & (11).

outupt phase
$$1 \Rightarrow V_{oA} = V_{iA}$$
 (9)

outupt phase
$$2 \Rightarrow V_{oB} = V_{iC}$$
 (10)

outupt phase
$$3 \Rightarrow V_{oC} = V_{iC}$$
 (11)

To achieve the pattern "6" we need to switch on the switches 1,8,9 as shown below representations both in matrix format in equation (12) and selector switch input single array format.

Control switch state 21
$$C = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 0 & 0 \\ 0 & 1 & 1 \end{bmatrix}$$
 (12)

 $selector\ array = [c1\ c1\ c3\ c4\ c5\ c6\ c7\ c8\ c9]$ (13)

WORKING OF FLC based MC-UPFC:

Fuzzy logic control based MC-UPFC based on direct power control having many advantages over the conventional ones. This controller has combined advantages of both fuzzy logic controller (FLC) and direct power controller based on space vector modulation. The FLC can work on non-linear system. The controller increases the robustness in the controlling system. There are two inputs for the fuzzy system and one output from it.

The two inputs to the FLC are injected voltage and its derivative. The input membership functions are designed using triangular shaped member functions (tri). The single output of the FLC is denoted by C-alpha and C-Beta and the output membership is designed by triangular shaped member function (tri). The corresponding membership functions are shown below.

A fuzzyfication is used to convert the input data into linguistic variable. A knowledge Base is data base with control rule set. A defuzzyfication interface which connects to a non-fuzzy control action.

The implementation of the fuzzy logic control based direct power control using sliding mode control using space vector modulation the input voltage (V_{in}) location and output Current (I_o) location has to be identified and it has to be correlated to 12 segments.

The actual power available in the controlled bus is measured and compared with the reference power given then error signal is calculated. The error signal was quantized using 3 level hysteresis comparator and it was mention as C-alpha. Similarly the reactive bus power has measured and compared with the reference signal and the error signal was quantized by using 3 level hysteresis comparator and denoted as C-beta.

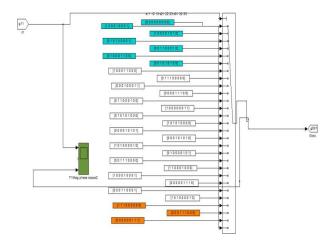


Figure-5 switching scheme selector switch.

Combination of C-alpha and C-beta can create 9(3x3) different possibilities and for each possibility we can match with the voltage segment (1-12) and current segment (1-12), based on the input reactive power error signal which has quantized into two level. Based on the above condition the switching state is selected and for the selection of the switching state fuzzy logic controller is implemented with two fuzzy controllers for real power control and reactive power control.

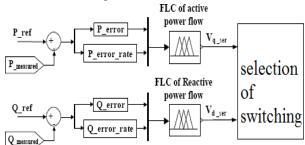


Figure: 6 block diagram of FLC in UPFC

The reference real power and reference reactive power will be fixed, the actual power quantities are measured and taken as input to the FLC as shown in the figure: 6, the direct errors and sliding surface errors $S_p(e_p,t)$ are measured and fed to the FLC which will gives the switching state accordingly, and selector switch will select the switching state.

The figure: 7 shows the implementation of FLC in quantizing the real power with the direct power error and sliding mode error, The error signal are feed to the FLC and the output values varies from high , low and zero.

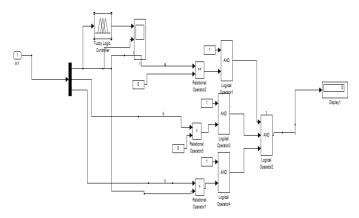


Figure:7 Implemented control circuit of FLC

To achieve the construction of FLC we need to frame the input membership functions as shown in the figure: 8(a). The member inputs are the direct power error quantities in real and reactive power. The membership functions are framed with triangular functions. The output functions are framed with the output member function as shown in the figure: 8(b).

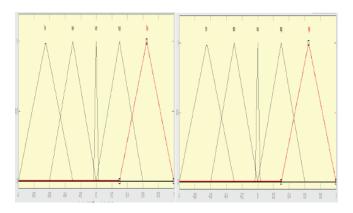


Figure: 8(a) FIS Fuzzy input member functions.

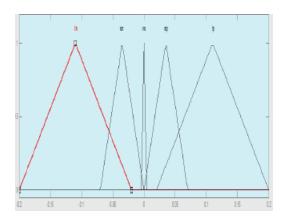


Figure: 8(b) FIS Fuzzy input member functions.

RESULTS:

The real power control in the 2 bus system is validated by constructing the simulation using MATLAB/Simulink Simpower System. The detailed design consisting of the blocks as matrix converter, generated voltage source, transmission system with shunt input transformer and series transformer with series filter is operated in the power graphical user interface. And its performance is monitored and highlighted with the input and output waveforms of the power parameters as input voltage, input current, real and reactive power. To reduce the simulation time Matrix converter was constructed with Ideal switches and the load with resistance and reactance as RL load is observed. With the resistive load the input power parameters are displayed in the figure: 9(a). the real power is constant also the reactive power. The output voltage and current wave form with the resistive load bus real and reactive power is shown in the output waveforms. Figure: 9 (b)

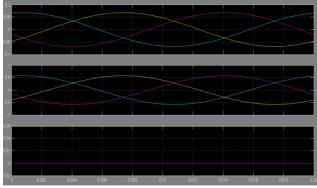


Figure: 9 (a) Input voltage, current, real power and reactive power.

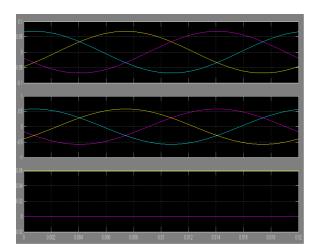


Figure: 9(b) Input voltage, current, real power and reactive power.

CONCLUSION:

In this paper, the real power flow control of the 2 bus transmission system is controlled using FLC based MC- UPFC is achieved with the sliding power control with direct power injection. Matrix converter based Unified Power Flow Controller has been developed using simulink tool Matlab simulator software. A efficient approach for designing Fuzzy logic controller for controlling the real power has been obtained for RL-impedance loading condition.. The use of MC-UPFC along with FLC provides better response and results in control of the real power with reference to the given reference power value in the power system.

In further this FLC algorithm can be developed for the 3 phase unbalanced loading condition. Also it has a scope of interconnected power flow controllers (IPFC).

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