IMPLEMENTATION OF VECTOR CONTROL FOR GRID VOLTAGE SUPPORT IN VSC-HVDC TRANSMISSION SYSTEM

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Abstract: This paper presents a performance study of voltage source converter high-voltage direct-current (VSC-HVDC) transmission system for supporting grid voltage under different operating conditions. The VSC-HVDC transmission system is used to integrate an off-shore wind power plant to grid. The vector control method is used to make a decoupled control of active and reactive powers for grid voltage disturbances mitigation. The dynamic behavior of the system is evaluated using MATLAB/SIMULINK package. Finally, the control method of the system integration into a small-scale grid side converter connected to a virtual-grid using the TMS320F28335 from Texas Instruments has been implemented. The simulation and experimental results were matched showing a good performance under different operating conditions.

Key words: HVDC transmission; vector control; off-shore wind power; grid integration; voltage-source converter.

1. Introduction

The recent decades have seen increased demand for reliable and clean forms of energy instead of conventional energy sources. Wind energy has shown the fastest rate of growth among different renewable energy sources that will reach to 666.1 GW of the cumulative worldwide capacity by 2019 (GWEC report, 2014) [1]. While, wind energy has some disadvantages such as the variation of the generated power for different locations and seasons due to the nature of the wind profile. Besides, good wind sites are often located far from cities in remote locations implying more cost, some impact on the environment because of the noise produced by the rotor blade and visual impact. Off-shore wind turbines overcome those drawbacks since the wind profile is uniform and far from the land. Consequently, future wind farms will be off-shore several hundred kilometers between the wind farm and the grid connection point.

High voltage direct current (HVDC) transmission may be the only feasible option for transmitting bulk power over long distances of more than 50 km with lower electrical losses than high voltage alternating current (HVAC). HVDC transmission systems have been built using two converter types: current source converter (CSC-HVDC), which uses line-commutated switching devices (thyristors) or voltage source converter (VSC-HVDC), which uses self-commutated switching devices such as IGBT, MOSFET, and GTO. Various HVDC configurations are summarized in [2-5] depending on the function and location of the converter station.

Electrical grid network comprises of different elements like generators, transmission lines and loads. Many disturbances could be occurred per a day to the grid network such as losing number of generators, transmission lines and/or loads variations. As a result, the grid voltage could be varied (increase/decrease) depending on the type of disturbance that might cause mal-operation or disconnection of other loads connected to the same grid. Therefore, the presence of a device that can generate/absorb reactive power at the grid terminal would help on mitigation of the grid voltage disturbances occurred.

In this paper, besides of being transmitting active power from the off-shore wind power plants, the on-shore side converter is used to compensate the reactive power needed for the voltage disturbance mitigation using vector control.

2. System description

Fig. 1 shows the proposed VSC-HVDC system that connects an off-shore wind power plant (WPP) of 300 MW rating to the main grid through a 100 Km HVDC transmission cables. The WPP (sending side) is aggregated and modelled as an ac source assuming that wind turbines are perfectly controlled to provide maximum power from the wind. System parameters are given in table 1 in the appendix.

- The phase reactor acts as a filter for the harmonic content produced by the converter switching. The main purpose of the reactor is to avoid a short circuit between the inverter and the grid. It also provides an independent control of active and reactive power by controlling the voltage drop and the current direction across it.
- A 350 MVA Delta/grounded-wye transformer is used to eliminate the third harmonic current generated by the converter.
In addition, the transformer maintains an effectively grounded system even when the distributed generation becomes islanded [6].

- Both on-shore and off-shore converter stations are two-level VSC using several modules of power semiconductors each comprising an IGBT with anti-parallel free-wheeling diodes. These two components are integrated in the same semiconductor package to provide current capability in the reverse direction and to prevent the application of reverse voltage.

- The dc side consists of two capacitor stacks of the same size while the middle point is grounded. The objective of the dc capacitors is to store energy for the power flow control and reduce the voltage ripple on the dc side. Capacitor stack size depends on two main factors: the amount of energy to be stored in the device and the time rate of change of the energy to be transferred into or out of the storage device. This depends mainly on the peak power rating of the power conversion unit as in equation (1). For the proposed model a 10 ms is chosen as a time rate of energy transfer (τ) which is used in capacitance calculation of equation (2) where Sn is the nominal MVA [7].

\[
W = \tau \cdot S_n = \frac{1}{2} CV_{dc}^2 \quad \text{(1)}
\]

\[
C = \frac{2\tau \cdot S_n}{V_{dc}^2} \quad \text{(2)}
\]

A. Modelling of Voltage Source Converter (VSC):

The dynamic model of a two-level sinusoidal Pulse-Width Modulation (SPWM) VSC in d-q synchronous reference frame is presented as discussed in the literature [8-10]. The two-level VSC shown in Fig. 2 represents both the on-shore converter which operates as inverter and the off-shore converter which operates as rectifier while the ac system represents either the grid or the WPP. The power flow between the ac system and the dc-link is bidirectional.

Fig. 2 Two-level Voltage Source Converter (VSC)

1) VSC in d-q synchronous reference frame:

Considering that the power is transferred from the ac system to the dc-link through the phase reactor \((R_aL_a)\) shown in Fig. 2 which represents the sum of the total resistance and inductance of the \(R_i\) filter and the transformer windings. The voltage drop across the phase reactor \(V_{L}(t)\) is given in equation (3) where \(V_{L}(t)\) is the three-phase ac system voltage and \(V_{C}(t)\) is the converter output voltage. Applying voltage invariant Clark transformation in stationary reference frame, equation (4) is derived.

\[
V_{f}(t) - V_{C}(t) = V_{L}(t) \quad \text{(3)}
\]

\[
V_{ca\theta} - V_{Cca\theta} = R_{Lad} \frac{di_{ad}}{dt} + L_{C} \frac{di_{dq}}{dt} \quad \text{(4)}
\]

By applying the Park’s transformation to (4) equation (5) is obtained where \(\omega\) is the ac system frequency in rad/sec.

\[
V_{id} - V_{Cdq} = R_{C} i_{id} + \omega L_{C} i_{dq} + L_{C} \frac{di_{dq}}{dt} \quad \text{(5)}
\]

The converter output voltage equations in the d-q synchronous reference frame can be written as:

\[
V_{cad} = V_{id} - R_{C} i_{d} - L_{C} \frac{di_{d}}{dt} - \omega L_{C} i_{q} \quad \text{(6)}
\]

\[
V_{caq} = V_{iq} - R_{C} i_{q} - L_{C} \frac{di_{q}}{dt} - \omega L_{C} i_{d} \quad \text{(7)}
\]

Where \(V_{id}\) and \(V_{iq}\) are the d-q ac system terminal voltages, \(V_{cad}\) and \(V_{caq}\) are the d-q converter voltages, and \(i_{d}\) and \(i_{q}\) are the d-q currents.

2) VSC active and reactive power in d-q synchronous reference frame:

The apparent power \(S\) at the point of common coupling (PCC) in d-q reference frame is given by equation (8) where the sign (*) represents the complex conjugation of the current vector.

\[
S = \frac{3}{2} V_{Vdq}^* V_{dq} = \frac{3}{2} (V_{id} + j V_{iq})(i_{d} - j i_{q}) \quad \text{(8)}
\]

Hence active and reactive power of the VSC measured at the PCC (ac side) are given respectively by:

\[
P = \frac{3}{2} (V_{id} i_{d} + V_{iq} i_{q}) \quad \text{(9)}
\]

\[
Q = \frac{3}{2} (V_{id} i_{q} - V_{iq} i_{d}) \quad \text{(10)}
\]

B. DC-link Power:

For a steady-state operation, active power exchange
at the ac side will be equal to the power exchange at the dc bus (neglecting the semiconductors losses) as in equations (11).

\[ P_{dc} = P_{ac} \]  
\[ V_{dc}I_{dc} = \frac{3}{2}(V_{td}i_d + V_{tq}i_q) \]

Then, the DC current is given by:

\[ I_{dc} = \frac{P_{ac}}{V_{dc}} = \frac{3(V_{td}i_d + V_{tq}i_q)}{2V_{dc}} \]  

Which means that the converter at the dc side will be seen as a constant current source of magnitude \( I_{dc} \). Therefore, when ac system is connected to VSC, the \( d-q \) reference frame is used to make a fully decoupled linear control of active and reactive currents.

3. Control strategy

The control strategy of HVDC transmission systems mainly depends on the application [11]. In this section the grid synchronization process is analysed and the control strategy of the proposed model for both on-shore and off-shore converter stations are presented.

A. Grid Synchronization:

In power converter interfacing renewable energy sources to the grid, the grid variables should be continuously monitored to ensure that the grid state is suitable for the correct operation of the power converter. There are several methods capable to detect the phase angle as discussed in [12]. The most commonly used method in three-phase systems is the synchronous frame Phase-Locked Loop (SF-PLL).

The block diagram of SF-PLL is illustrated in Fig. 3, where the three phase voltages are sensed and the instantaneous phase angle \( \theta \) is detected by synchronizing the PLL rotating reference frame to the grid voltage vector.

![Fig. 3 Phase locked loop block diagram](image_url)

The grid voltage has been chosen to be aligned with \( d \)-axis i.e. the \( q \)-axis voltage is zero while the \( d \)-axis voltage has the grid voltage magnitude. This is done by using a PI controller to force the quadrature axis to reach zero value. A feed-forward term \( \omega_0 \) is introduced to improve the overall tracking performance of the PLL where \( \omega_0 \) is the frequency of the grid. Finally, the frequency is integrated to obtain the grid voltage angle \( \theta \).

B. On-shore converter station control:

One of the advantages of VSC-HVDC is that it makes possible to independently control the active power and the reactive power. The active power depends on the power generated from the off-side that can be controlled either directly or by using a droop characteristics which called (DC droop control) [13]. The reactive power under normal operating condition is set to zero for a unity power factor operation. In some applications, the reactive power can be controlled to compensate for the reactive power demand or support the grid ac voltage when it drops under certain limits depending on each country grid code (e.g. Static synchronous compensator (STATCOM) [14]).

Recalling again equations (9) and (10) while the grid voltage is aligned into \( d \)-axis i.e. \( V_{td} = V_t, V_{tq} = 0 \) equations of active and reactive power can be given by:

\[ P = \frac{3}{2}V_{td}i_d \]  
\[ Q = -\frac{3}{2}V_{td}i_q \]

It is clear that the active and reactive power are controlled through the two-decoupled currents \( i_d \) and \( i_q \). The control loops of the on-shore converter station consist of two control loops, power and current loops as shown in Fig. 4. The outer loop (power loop) compares the reference values of active and reactive power \( (P_r, Q_r) \) to the measured values. The error is controlled using a proportional-integral (PI) controller which are the current references \( (i_{dr}, i_{dq}) \). The current references are compared with the actual currents which are controlled using a PI controller providing the reference voltage across the reactor.

The converter reference voltages \( (V_{Cdr}, V_{Cdq}) \) in dq-synchronous frame are transformed to abc-three phase voltages for PWM. The two terms \( \omega_0L_{adj} \) and \( \omega_0L_{adj} \) are used to compensate for the coupling between the \( d \)-and \( q \)-axis.

![Fig. 4 On-shore station closed loop control system](image_url)

C. Off-shore converter station control:

The off-shore converter control goal is to keep the dc-link voltage constant at its reference value. Consequently, the active power is controlled according to equation (13) by controlling the \( d \)-axis current. The reactive power is often set to zero for unity power factor operation.
The off-shore converter control loop consists of two control loops; the inner loop is a current regulator and the outer loop is a voltage regulator as shown in Fig. 5. The dc bus voltage is sensed and compared to the reference value then the error signal is regulated by a PI regulator. The output of the voltage regulator (the reference d-axis current) is compared to the measured d-axis current. Finally, the current error is regulated through a PI current regulator that gives the reference voltage across the reactor as mentioned before. The reactive power control consists of a current regulator to ensure that the q-axis current is set to zero.

4. Simulation Results

The behaviour of the VSC-HVDC transmission system has been analysed in different conditions. The system was simulated using MATLAB/SIMULINK package. The IGBT modules were simplified as one IGBT module for the top and another for the bottom half of each leg. The capacitor bank for the dc-link was simplified to one capacitor for the positive and negative dc-link voltage. The switching frequency has been chosen as 2.5 kHz. Two cases were analysed in order to evaluate the behaviour of the system as follow:

A. CASE1: Sending only active power (P):

For wind power applications, the active power exchange is unidirectional, the power can be transferred from the off-shore to the grid side. At t=0.2 s the reference active power sent to the grid was set to 200 MW i.e. (0.67 p.u) till 0.4 the reference was increased into 1 p.u. At t=0.6 s the reference active power has been decreased again to 0.67 p.u and finally to zero. The reference reactive power was set to zero for unity power factor operation. Fig. 6.a shows the reference and measured active and reactive power injected to the grid. As shown in figure, the current has increased to 1 p.u at 0.4 s to deliver the rated active power demand.

B. CASE2: Sending only reactive power (Q):

The behaviour of the VSC-HVDC system was analysed while sending only reactive power with zero active power injection. In order to evaluate the controller performance of the inverter, the reactive power was set to -100 MVAR i.e. (0.33 p.u) at t=0.2 s then changed to +0.33 p.u at t=0.4 s, -0.33 p.u again at t=0.6 s and finally to zero at t=0.5 s. The active power was set to be zero for evaluation of reactive component only. Fig. 8.a shows a good performance of the...
controller. The measured power followed the reference having 20 ms of settling time with zero-steady state error and no overshoot in both directions.

The three phase grid currents as in Fig. 8.b show the dynamic behavior of currents while changing direction of reactive power flow from negative (sending) to positive (receiving). From Fig. 8.c, the dc-link voltage is not affected by the reactive power exchange.

5. Experimental setup

The behavior of the on-shore inverter side was verified experimentally through a small-scale inverter supplied by a fixed dc source. The three-phase inverter was connected to a virtual grid i.e. the grid voltage was sensed to obtain the grid angle but the inverter output was connected to a three-phase RL load. The voltages and currents of phase A and B were sensed then sent to the controller board as shown in Fig. 10. The controller board consists of three stages as following:

A. Input interface:

The received signals from the current and voltage sensors were filtered from high-frequency noise produced by the switching of the IGBTs then adapted to take values between 0 to 3V dc.

B. Output interface:

The output interface circuit is the drive circuit which adapts the Pulse-Width Modulation (PWM) output signals of the controller to the operating voltage of the IGBT drivers. The MTL-CB10010N12IXFE Guash power stack requires +15V dc gate signals.

C. Digital Signal Processor (DSP):

The output signals from the input interface were connected to the A/D module of the DSP TMS320F28335. The potentiometers included in the board were used to set the active and reactive power references. The overall hardware implementation of the three-phase inverter connected to a virtual grid is shown in Fig. 11.
6. Experimental Results

The inverter output was connected to an RL load while the grid angle was obtained by sensing the grid voltage. As a result, the current direction has to be the inverse of the simulation results as shown in Fig. 14. For the first case only the active power was applied to the controller which results of having the current in phase with the grid voltage as seen in Fig. 12.(a). While in the second case the system with setting a reference of positive and negative reactive power. The current with the grid voltage waveforms while sending (+Q) and (-Q) are shown in Fig. 12.(b) and Fig. 12.(c) respectively.

![Figure 12 Phase A current and voltage measured under operating conditions.](image)

7. Conclusions

The paper studies the integration performance of offshore wind power plants through the HVDC-VSC based transmission. In addition, the vector control of the grid side converter was implemented showing that the experimental and simulation results were matched for independently control of active and reactive power. Consequently, the inverter reactive power can be controlled to compensate the reactive power change at the PCC. Which in return helps to mitigate the grid voltage disturbances to keep grid voltage within limits.

The system implementation can be used to study the factors affecting on the grid integration of renewable energy systems using VSCs. Grid impedance, grid angle and phase locked loop are most common factors affecting on the integration process that are still an interesting topic under investigation.

**Appendix**

<table>
<thead>
<tr>
<th>Table 1: System Parameters</th>
<th>Value</th>
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</thead>
<tbody>
<tr>
<td>Parameter</td>
<td>Value</td>
</tr>
<tr>
<td>Reactor resistance, $R_p/R_{ref}$</td>
<td>0.8 $\Omega$</td>
</tr>
<tr>
<td>Reactor inductance, $L_p/L_{ref}$</td>
<td>1 mH</td>
</tr>
<tr>
<td>DC-link Capacitor, $C$</td>
<td>300 $\mu$F</td>
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<tr>
<td>DC-cable resistance, $R_{dc}$</td>
<td>0.01 $\Omega$/Km</td>
</tr>
<tr>
<td>DC-cable inductance, $L_{dc}$</td>
<td>0.01 mH/Km</td>
</tr>
</tbody>
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**References**