DESIGN OF NOVEL HYBRID SHUNT ACTIVE FILTER FOR THE IMPROVEMENT OF POWER QUALITY IN DISTRIBUTION LINES

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ABSTRACT

This paper deals with the hysteresis current control based hybrid shunt active filter to improve the quality of power in distribution line by minimizing the harmonics. The main harmonic sources of distribution lines are the non linear loads and frequent switching of industrial loads. The non linear loads take discontinuous current and thus it injects harmonics. The switching of loads produces voltage sag and swells which leads to harmonics in the lines. The proposed hybrid shunt active filter is fully characterized by series LC and shunt DC link connected through 3-phase active filter. The disturbances in the supply voltage and load current due to frequent switching of a 5 HP wound induction motor in the 415 V, 50 Hz distribution line are observed using CW240 power quality meter. The same is simulated in MATLAB with and without filter. The simulation results obtained from the proposed method proves that it gives comparatively better THD value.

KEYWORDS— Hysteresis Current Control [HCC], Active Power Filter [APF], Distributed Generators [DG], Distribution Systems [DS], shunt active filters, Voltage Regulation [VR] and Total Harmonic Distortion [THD].

I. INTRODUCTION

Harmonic pollution and reactive power in the power system are the important power quality problems. With the proliferation of non-linear loads in industrial applications and frequent switching of loads in the distribution systems, the compensation of harmonic and reactive power is becoming increasingly concerned. Shunt passive filters have been widely used because of their low cost and low loss. However, the performances of the filters are very sensitive to the power system impedance and series or parallel resonance with the power system impedance may occur. Also, the effective compensation with the variation of the voltage cannot be carried out with passive filters. The filter performance of shunt active power filter does not depend on the power system impedance or any other constant parameters. The compensation of harmonic and reactive power can be achieved dynamically in the case of APF. The research in this field has been done for many years and researchers proposed several methods of improving THD value up to 4% by eliminating harmonics selectively or reducing it by using different filtering techniques in the active filters. The References papers have reported field test results of active filters intended for installation on power distribution systems. The active filter is characterized by behaving like a resistor for harmonic frequencies, resulting in damping out the harmonic amplification throughout a distribution line. Since the proposed distribution system consists of four distribution lines, installing the active filter on the end bus of each line is effective in harmonic damping. A static synchronous compensator is one of the most effective solutions to regulate the line voltage. However, no literature has addressed the dynamic behaviour of the active filter when it performs both harmonic damping and voltage regulation at the same time. The reactive power flow, the voltage and current variations due to frequent switching of 5 HP wound induction motor in the 415 V, 50 Hz distribution system are observed using power quality meter and same is verified in the simulation. The novel hybrid shunt active filter is designed and simulated in MATLAB for same application. The simulation results are shown to verify the effectiveness of the active filter capable of harmonic damping.

II. HYBRID SHUNT ACTIVE FILTER FOR A DISTRIBUTION SYSTEM

Fig1: A Distribution system with filter [one line circuit]
A. Harmonic Amplification & Damping

The inductor in Fig. 1 is the resultant of a leakage inductance of a distribution transformer to line inductances. The capacitors are the PFC capacitors installed by consumers. It is observed that the fifth and seventh harmonic voltages present in the system. The “harmonic amplification” is resulting from resonance between the inductance and the capacitance on the line and its effect is more during day time compared to night time. The shunt active filter for damping out harmonic propagation is connected to a distribution system. Fig. 2 shows the simplified Distribution System having a shunt hybrid active filter proposed in this paper. The transformer in the pure filter is replaced by a capacitor in the hybrid filter. The purpose of the capacitor is to impose high impedance to the fundamental frequency so that the fundamental voltage appears exclusively across the capacitor. This means that no fundamental voltage is applied across the active filter.

![Fig 2. A typical 3 Φ Hybrid Shunt Active filter](image)

Fig 2 shows the detailed power circuit of the hybrid filter, which consists of a three-phase voltage-fed PWM inverter and [switches are named as per their conduction sequence] a series connection of L and C per phase. Note that the tuned frequency of L and C is not the fifth-harmonic frequency but around the seventh-harmonic frequency. The reason is that the seventh-tuned LC filter is less bulky than the fifth-tuned LC filter as long as both filters have the same inductor as L. The dc-bus of the PWM inverter has only a capacitor without external supply, and the dc-bus voltage is controlled by the hybrid filter. The hybrid filter is controlled so as to draw the compensating current i_c from the distribution line.

B. Operating Principle

The compensating current of the hybrid shunt active filter consists of a fundamental component and harmonic components and they are determined by the impedance of the LC filter and are controlled by the active filter. Three-phase voltages and currents are detected at the installation bus. The harmonic voltage V_h in each phase is extracted from the detected three-phase voltage, and then the harmonic voltage is amplified by a control gain K_v. Thus the harmonic current reference i_h is given by

$$i_h = K_v \cdot V_h$$  \hspace{1cm} (1)

The actual harmonic compensating current i_h is extracted from the detected compensating current i_c. Assuming that it is equal to its reference i_h, the hybrid filter behaves as a damping resistor of 1/K_v [V/A] for harmonic frequencies. The optimal value of K_v is equal to the inverse of the characteristic impedance of the distribution feeder. With this value, the hybrid filter can damp out harmonic propagation effectively.

III. EXPERIMENTAL SYSTEM [proposed]

![Fig 3: Single line diagram of experimental system](image)

In the experimental the harmonics are generated either by switching the load frequently or by connecting a non linear load. A three-phase power distribution feeder simulator rated at 230 V, 50 Hz, and 20 kW is used for the laboratory experiments. Table I summarizes the line simulator parameters. When a lossless line is assumed, the characteristic impedance of the feeder simulator Z_o can be calculated as

$$Z_o = \sqrt{L/C}$$  \hspace{1cm} (2)

A shunt hybrid active filter component values given in the table – II is connected to the bus. The dc-bus voltage of the PWM inverter is controlled to be 40 V. Note that the dc-bus voltage as low as 40 V in the 230-V system corresponds to a dc-bus voltage as low as 1.3 kV. This means that adopting a diode-clamped three-level inverter allows us to use 1200-V IGBTs that are easily available on the market at low cost. Referring to [7], the control gain K_v should be set to the inverse of the characteristic impedance of the feeder.

<table>
<thead>
<tr>
<th>TABLE 1: Parameters of the feeder simulator</th>
</tr>
</thead>
<tbody>
<tr>
<td>Line inductance</td>
</tr>
<tr>
<td>Line Resistance</td>
</tr>
<tr>
<td>Line Capacitance</td>
</tr>
</tbody>
</table>
TABLE II: Parameters of the Hybrid Filter

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>LC Filter</td>
<td>1.3 KVA</td>
</tr>
<tr>
<td>L</td>
<td>2 mH</td>
</tr>
<tr>
<td>C</td>
<td>100 µF</td>
</tr>
<tr>
<td>Resonant Frequency</td>
<td>356 Hz</td>
</tr>
<tr>
<td>Quality Factor</td>
<td>Q = 20</td>
</tr>
<tr>
<td>DC Bus Capacitor</td>
<td>Cdc = 4700 µF</td>
</tr>
<tr>
<td>DC Bus Voltage</td>
<td>Vdc = 40 V</td>
</tr>
</tbody>
</table>

III. HCC PRINCIPLE

The bidirectional switches shown in Fig. 2 are controlled with hysteresis current control technique. In the balanced three phase system and unity power factor operation, the three-phase voltages and reference currents are equal in magnitude and displaced by 120°. The switches \( S_1, S_3, \) and \( S_5 \) conduct when corresponding Phase voltage is in positive half cycle and the bidirectional switch \( S_4 \) is in the switch-off state. Similarly, switches \( S_2, S_4, \) and \( S_6 \) conduct when corresponding phase voltage is in negative half cycle and the bidirectional switch is in the switch-off state. In order to prove the instantaneous power balance between ac source and dc bus, the local average method is adopted [5]–[6]. In Fig 4, phase a current \( i_a \) is controlled to track the reference current within the window width \( i_{w} \) and same way the other two phases are carried out.

The phase a current being switched between upper and lower bounds to track the reference current (upper trace); Switch \( S_1 \) current (mid trace); bidirectional switch \( S_4 \) current (lower trace) in the balanced three-phase system and unity power factor operation.

A. Operation during the Positive Interval of \( i_{\text{refa}} \)

It is explained by taking phase “a” parameters and the other phase parameters are assumed to be same. During \( 0 < t < t_1 \), the supply current \( i_1 \) flows through the bidirectional switch \( S_4 \), rising from \( i_{\text{refa}} - 0.5I_a \) to \( i_{\text{refa}} + 0.5I_a \) and during \( t_1 < t < (t_1 + t_2) \), the switch \( S_4 \) is off and the currents in the input inductor continues to flow through the freewheeling diode D1. It is assumed that the time period \( t_1-t_2 \) is so concise that \( i_{\text{refa}} \) is considerably constant. From Fig. 3 Let \( i_{\text{refa}} \) be the current flowing through the upper switch \( S_1 \) and \( i_{\text{refa}} \) be the current flowing through the bidirectional switch \( S_4 \). The average currents \( i_{\text{refa}} \) and \( i_{\text{refa}} \) are

\[
\begin{align*}
\frac{1}{t_1-t_2} \int_{t_1}^{t_2} i_{\text{refa}} \, dt &= \frac{i_{\text{refa}} + i_{\text{refa}}}{2} \quad \text{(3)} \\
\frac{1}{t_1-t_2} \int_{t_1}^{t_2} i_{\text{refa}} \, dt &= \frac{i_{\text{refa}} + i_{\text{refa}}}{2} \quad \text{(4)}
\end{align*}
\]

When the bidirectional switch \( S_4 \) is on, the corresponding equation is

\[
\frac{1}{t_1-t_2} \int_{t_1}^{t_2} i_{\text{refa}} \, dt = I_{\text{refa}} \quad \text{(5)}
\]

Since the phase a current \( i_a \) rises from \( i_{\text{refa}} - 0.5I_a \) to \( i_{\text{refa}} + 0.5I_a \) within time \( t_1 \), hence (5) can be expressed as

\[
L_{\text{dc}} \frac{dI_{\text{refa}}}{dt} = v_a \quad \text{(6)}
\]

From (6) \( t_1 \) can be expressed as

\[
t_1 = \frac{L_{\text{dc}} I_{\text{refa}}}{v_a} \quad \text{(7)}
\]

When bidirectional switch \( S_4 \) is switched off and diode \( D_1 \) is on, phase a current \( i_a \) falls from \( i_{\text{refa}} + 0.5I_a \) to \( i_{\text{refa}} - 0.5I_a \) within the transit time \( t_2 \)

\[
\frac{1}{t_1-t_2} \int_{t_1}^{t_2} i_{\text{refa}} \, dt = v_a - 0.5V_{\text{dc}} \quad \text{(8)}
\]

From (8) \( t_2 = \frac{L_{\text{dc}} I_{\text{refa}}}{v_a} \quad \text{(9)}
\]

But \( 0.5V_{\text{dc}} > v_a \); Substituting (7) and (9) in (8)\)

\[
i_{\text{refa}} = \frac{L_{\text{dc}} I_{\text{refa}}}{v_a} \quad \text{(10)}
\]

B. Operation during the Negative Interval of \( i_{\text{refa}} \)

During this condition, the phase a voltage is in negative half cycle and the Switch \( S_1 \) is reverse-biased and the phase current will flow through Switch \( S_2 \). It is similar to the operation of positive interval of \( i_{\text{refa}} \).

Similar to expression (10), \( i_{\text{refa}} \) is also calculated as

\[
i_{\text{refa}} = \frac{L_{\text{dc}} I_{\text{refa}}}{v_a} \quad \text{(11)}
\]

Similarly the phase b and phase c currents can be obtained as

\[
\begin{align*}
i_{\text{refb}} &= \frac{2V_{\text{dc}}}{V_{\text{dc}} + 2I_{\text{refb}} I_{\text{refb}}} \quad (i_{\text{refb}} \geq 0) \quad \text{(12)} \\
i_{\text{refb}} &= \frac{0}{I_{\text{refb}} > 0} \quad (i_{\text{refb}} \\n0 &< 0) \quad (i_{\text{refb}} \geq 0) \\
i_{\text{refc}} &= \frac{2V_{\text{dc}}}{2V_{\text{dc}} + 2I_{\text{refc}} I_{\text{refc}}} \quad (i_{\text{refc}} \geq 0) \quad \text{(14)} \\
i_{\text{refc}} &= \frac{0}{I_{\text{refc}} > 0} \quad (i_{\text{refc}} \geq 0) \\
i_{\text{refc}} &= \frac{0}{I_{\text{refc}} < 0} \quad (i_{\text{refc}} \geq 0) \quad \text{(15)}
\end{align*}
\]

The sum of the upper diode currents \( i_{\text{refa}}, i_{\text{refb}}, i_{\text{refc}} \) gives the upper dc link currents \( i_1 \) and the sum of the lower diode currents \( i_{\text{refa}}, i_{\text{refb}}, i_{\text{refc}} \) gives the lower dc link currents. The total instantaneous dc power \( P_{\text{dc}} \) is

\[
P_{\text{dc}} = \frac{1}{2} \int_{t_1}^{t_2} i_{\text{refa}} \, dt + \frac{1}{2} \int_{t_1}^{t_2} i_{\text{refb}} \, dt + \frac{1}{2} \int_{t_1}^{t_2} i_{\text{refc}} \, dt = v_a i_{\text{refa}} + v_b i_{\text{refb}} + v_c i_{\text{refc}} \quad \text{(16)}
\]
Without considering the losses for a balanced three phase system with unity power the instantaneous power can be expressed as
\[ p_{dc} = 3V_p I_{ref} = p_{ac} \]  \hspace{1cm} (17)

The reference supply current under unity power factor can be obtained as
\[ i_{ref} = \frac{V_p}{3I_{dc}} \]  \hspace{1cm} (18)

Thus whenever there is any variation on the load the reference current can be adjusted by the output power estimator and dc link voltage regulator, the ac side neutral current \( i_{n1} \) can be zero in magnitude and expressed as
\[ i_{n1} = v2i_{ref} [\sin(\omega t) + \sin(\omega t - \frac{2\pi}{3}) + \sin(\omega t + \frac{2\pi}{3})] \]  \hspace{1cm} (19)

Substituting \( i_{ref} \) in \( i_1 \) and \( i_2 \) and summing them the injection current \( i_{n2} \) is obtained. Thus \( i_{n2} \) consists of the sum of the currents through three bidirectional switches, and is given as
\[ i_{n2} = i_2 - i_1 = \sin(3\omega t) \]  \hspace{1cm} (20)

Thus the expressions 18, 19 and 20 are used to calculate the amount of current to be injected in order to minimize the harmonics. The same can be implemented in simulation model.

IV. TEST RESULTS

A 5HP induction motor is connected to the three phase 415V, 50Hz distribution system through a switch. The motor is switched on and off frequently to monitor the voltage sag and swell, current variations and reactive power flow. During the test period the waveforms of load current and voltage also monitored using Yokogawa CW240 power quality analyser. The results of them are given below and are taken by using print screen option in the instrument.

<table>
<thead>
<tr>
<th>LOAD INST.</th>
<th>DATE/ (3P)</th>
</tr>
</thead>
<tbody>
<tr>
<td>U1 228.2 V</td>
<td>09/29</td>
</tr>
<tr>
<td>U2 229.8 V</td>
<td>09/29</td>
</tr>
<tr>
<td>U3 227.5 V</td>
<td>09/30</td>
</tr>
<tr>
<td>Uave 230.0 V</td>
<td>09/30</td>
</tr>
</tbody>
</table>

The above readings are the values of supply voltage, motor current, real and reactive power, power factor and frequency under normal condition. All of the values indicate that the variations are within the acceptable limit.

V. CONTROLLER DESIGN

The load current samples are fed to the PLL to generate their fundamentals and are transferred from three phase to two phase quantities. The ripples are filtered by LPF and then they are transformed to three phase quantities. This reference current \( i_{refa}, i_{refb}, i_{refc} \) is added with the load current \( i_a, i_b, i_c \) by comparator. The error generated is then sending to the HCC. The hysteresis current controller (HCC) generates pulses based on the current window width (Iw).

The below table gives the values of voltage sag and swell occurred during the motor switched on.

<table>
<thead>
<tr>
<th>DATE/ (3P)</th>
</tr>
</thead>
<tbody>
<tr>
<td>09/29</td>
</tr>
<tr>
<td>09/29</td>
</tr>
<tr>
<td>09/30</td>
</tr>
<tr>
<td>09/30</td>
</tr>
</tbody>
</table>

The voltage has gone to minimum of 45V from 230V for a moment and attains to its normal value. This phenomenon leads to a harmonic injection according to the Fourier analysis.

The current waveforms are much deteriorated from the supply voltage waveforms which clearly indicate the harmonics due to switching of loads. Hence in the distribution system the harmonic sources are the non linear loads and switching of industrial loads. These are taken as a case for simulation and the results are obtained shows the scope of the paper.
The pulses are used to trigger the switches in the filter such a way that the filter observes the harmonics when the error current exceeds the window width and it injects the current \( i_{n2} \) when the error current is below \( I_w \). Thus the harmonics in the load currents are minimised much better than the methods like sliding mode control, shunt or series active filters etc. The simulation model and its results are given in the following figures.

![Fig 5 Block Diagram of Triggering Pulse Generator](image)

**VII. SIMULATION RESULTS**

The THD value without filter is 22.91%. The large amount of harmonics in the distribution line produces high value of THD. This value may vary time to time due to switching of new loads and their types. Hence a dynamic filtering system is required to reduce the harmonics.

![FFT window of load current without filter](image)
The HCC based hybrid shunt active filter is connected to the distribution system and simulated. The THD value has reduced to as low as 3.46%. The result is given below.

![Graph showing THD reduction to 3.46%]

**VIII. CONCLUSIONS**

The harmonic pollutions in the 415V, 50Hz distribution system due non linear loads and the switching of industrial loads are analysed with the simulation and measured results. The HCC principle and its implementation are explained with mathematical proof. The dynamically produced triggering pulses for the hybrid shunt active power filter fetches good results for this method. The results of this method can be validated and it will be the future scope of this study.

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